

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

h re patent application of

Buffet et al.

Serial No.:

10/665,321

Group Art Unit:

3679

Filed:

September 20, 2003

Examiner:

Not Assigned

For:

METHOD FOR LOCATING IDDQ DEFECTS USING MULTIPLE

CONTROLLED COLLAPSE CHIP CONNECTIONS CURRENT

MEASUREMENT ON AN AUTOMATIC TESTER

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

STATUS INQUIRY

Sir:

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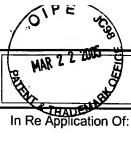
The above-identified patent application was filed on September 20, 2003. As of this date, a first action on the merits has not been received by this office and it is respectfully requested that the undersigned be provided with a statement of the status of this application.

Respectfully submitted,

Michael E. Whitham Reg. No. 32,635

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Customer Number 46170



TRANSMITTAL LETTER (General - Patent Pending)

Docket No. BUR9-2000-0229US2

In Re Application Of: Buffet et al.

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/665,321	09/20/2003	Not Assigned	46170	3679	6241

Title: METHOD FOR LOCATING IDDQ DEFECTS USING MULTIPLE CONTROLLED COLLAPSE CHIP CONNECTIONS CURRENT MEASUREMENT ON AN AUTOMATIC TESTER

COMMISSIONER FOR PATENTS:

Transmitted herewith is:

Status Inquiry Postcard

in the above identified application.

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is attached.

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Signature

Dated: March 22, 2005

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

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